

WHAT IS CLAIMED IS:

1. A chip carrier comprising:

5 a carrier base having an opening and being capable of
accommodating a chip inside the opening; and
an outer lid for closing the opening of the carrier base,
wherein the outer lid is engaged with the carrier base
when rotated in a space of the carrier base formed over the
opening.

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2. The chip carrier according to claim 1, wherein the
carrier base has a pair of erect portions having respective
inside surfaces, the erect portions being opposed to each other
with the space formed in between, and wherein the outer lid
15 has a pair of side surfaces and is engaged with the carrier
base in such a manner that the side surfaces of the outer lid
slide on the associated, respective inside surfaces of the erect
portions.

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3. The chip carrier according to claim 2, wherein the
inside surfaces of the erect portions of the carrier base are
formed with respective projections or recesses, and wherein
the side surfaces of the outer lid are formed with respective
recesses or projections to be engaged with the associated,
25 respective projections or recesses of the erect portions of
the carrier base.

4. The chip carrier according to claim 2, wherein the
inside surfaces of the carrier base and the side surfaces of
30 the outer lid have such arc shapes that the outer lid can fit
in the carrier base.

5. The chip carrier according to claim 1, wherein the carrier base has, around the opening, at least three erect portions having inside surfaces that are formed with projections or recesses, and wherein the outer lid has side surfaces that
5 are formed with recesses or projections to be engaged with the associated, respective projections or recesses of the erect portions of the carrier base.

6. The chip carrier according to claim 1, further
10 comprising an inner lid to be disposed between the outer lid and the chip to be accommodated in the carrier base.

7. The chip carrier according to claim 6, wherein a surface of the outer lid to be opposed to the inner lid is formed
15 with a projection on a rotation axis of the outer lid.

8. The chip carrier according to claim 6, wherein a surface of the inner lid to be opposed to the outer lid is formed with a projection at a position to be located on a rotation
20 axis of the outer lid.

9. A method of testing a chip through use of the chip carrier defined in claim 1.